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Properties of junction diodes under conditions of bisotropic strains

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Abstract. In consideration of influence of technological strains on characteristics of junction diodes located on surface of silicon wafers, biaxial character of such strains has taken into account. The cases of (001)- and (111)-oriented silicon wafers on whose surface the diodes are located as well as longitudinal (in plane of the wafer) and transversal (perpendicular to it) diode current flow have been considered. It is shown that at small strains, the diodes located on the surface of (111)-Si are less subjected (as a whole) to their influence. Furthermore, the change in the intrinsic carrier concentration has been assessed in (001)-Si at very large bisotropic strains. The concentration is shown to increase by several orders of magnitude irrespective of the strain sign.

Keywords: junction diode, bisotropic strain, piezjunction effect, silicon, SiGe alloy.

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1. Introduction

In the papers [1-6], an influence of mechanical stresses on characteristics of $p-n$ diodes and transistors (piezjunction effect) was studied. In a number of cases, motivation of these investigations was to study effect of technological deformations on performance of $p-n$ diodes (or transistors) used as temperature sensors. Herewith the mechanical stresses were considered as uniaxial (sometimes as hydrostatic).

In the same time, it is clear that in multi-layer structures (diodes, transistors) which consist of a substrate, doped layers, passivating and metallic coatings, etc., biaxial deformations develop acting in plane of the layers. Of course, in response to this action, the uniaxial strain (as a rule, of opposite sign) appears in direction perpendicular to the layers, but initial (given) deformation is still biaxial. Character of the initial strain manifests itself quantitatively and even sometimes qualitatively (see, for example, [7]). Let us notice also that there are mechanical systems which develop controllable external biaxial stresses [8-10].

Only one paper [11] is known to the author, which proceeds from biaxial character of the strain acting to a $p-n$ junction. But in the paper [11], a case of silicon oriented only in [001] direction has been considered and electron transport was assumed in the same direction.

In this paper, an influence of small biaxial isotropic (bisotropic [12]) strains on properties of $p-n$ diodes located on surface of silicon wafer both (001)- and (111)-oriented has been studied theoretically. The effect depends essentially on the wafer orientation and current direction. Furthermore, for (001)-oriented silicon the case of very large bisotropic strains has been considered.

2. Starting relations

As it has been shown by us in papers [4, 5] effect of deformation on magnitude of a voltage drop across a diode, through which (under its use as temperature sensor) a constant stabilized current is conducted, is analyzed conveniently by means of a product

$$\frac{n_{\min or}(\hat{u}) \mu_{\min or}(\hat{u})}{n_{\min or}(0) \mu_{\min or}(0)} = \frac{n_i^2(\hat{u}) \mu_{\min or}(\hat{u})}{n_i^2(0) \mu_{\min or}(0)} \quad (1)$$

where $n_{\min or}(\hat{u})$ and $\mu_{\min or}(\hat{u})$ are concentration and mobility of minority carriers in the diode base depending on the strain tensor \hat{u} , $n_i(\hat{u})$ is intrinsic carrier concentration in diode material.

When both ratios in (1) vary with strain in the same direction (i.e. together increase or together decrease), the effect is particularly large. But if they vary in opposite manner, then two components of piezjunction effect

(i.e. changes in concentration and mobility) compensate each other to a high degree. Variation in the voltage drop ΔU across the diode at the expense of the strain is expressed in the terms of (1) in the form

$$\Delta U(\hat{u}) = -\frac{kT}{q} \ln \left[\frac{n_i^2(\hat{u}) \mu_{\text{minor}}(\hat{u})}{n_i^2(0) \mu_{\text{minor}}(0)} \right], \quad (2)$$

where k is the Boltzmann constant, T is temperature, q is the elementary charge. Here and below, we consider only n^+p silicon diode with a short base (when the base thickness is noticeably less than the diffusion length of the minority electrons).

3. The case of (001)-oriented silicon wafer

In this case $u_{xx} = u_{yy} = u_{II}$, $u_{zz} = u_{\perp} = \frac{2S_{12}}{S_{11} + S_{12}} u_{II}$

(S_{ij} are the components of the elastic compliance matrix), and all off-diagonal elements of the strain tensor are equal to zero. Herewith 3 pairs of electron valleys located along [100], [010], and [001] axes acquire following shifts in the energy

$$\delta E_c^{(100)} = \delta E_c^{(010)} = \Xi_d(2u_{\parallel} + u_{\perp}) + \Xi_u u_{\parallel}, \quad (3)$$

$$\delta E_c^{(001)} = \Xi_d(2u_{\parallel} + u_{\perp}) + \Xi_u u_{\perp},$$

where Ξ_d and Ξ_u are deformation potentials for the conduction band.

For the shifts in the energy of different branches of the valence band at wave vector $\vec{k}=0$ (h for heavy holes, l for light holes, $s-o$ for split-off holes), we have (in accordance with [13, 14])

$$\delta E_v^h = a(2u_{\parallel} + u_{\perp}) + \varepsilon,$$

$$\delta E_v^l = a(2u_{\parallel} + u_{\perp}) - \left(\varepsilon + \Delta_{s-o} - \sqrt{9\varepsilon^2 + \Delta_{s-o}^2 - 2\varepsilon\Delta_{s-o}} \right) / 2, \quad (4)$$

$$\delta E_v^{s-o} = a(2u_{\parallel} + u_{\perp}) - \left(\varepsilon + \Delta_{s-o} + \sqrt{9\varepsilon^2 + \Delta_{s-o}^2 - 2\varepsilon\Delta_{s-o}} \right) / 2 - \Delta_{s-o},$$

where $\varepsilon = -b(u_{\parallel} - u_{\perp})$, Δ_{s-o} is the energy of spin-orbital splitting of the valence band, a and b are the deformation potentials for the valence band.

By means of the self-consistent procedure of calculating the Fermi-energy shift with deformation described in [2], we obtain

$$\frac{n_i^2(\hat{u})}{n_i^2(0)} = \frac{1}{3} \left[\frac{m_h^{3/2} \exp(\delta E_v^h / kT) + m_l^{3/2} \exp(\delta E_v^l / kT) + m_{s-o}^{3/2} \exp((\delta E_v^{s-o} - \Delta_{s-o}) / kT)}{m_h^{3/2} + m_l^{3/2} + m_{s-o}^{3/2} \exp(-\Delta_{s-o} / kT)} \right] \sum_{m=1}^3 \exp(-\delta E_c^{(m)} / kT) \quad (5)$$

where m_h , m_l and m_{s-o} are density-of-state effective masses for heavy, light and split-off holes, respectively, whose variation with the strain at considered here small deformation is neglected (see [4, 5]).

Mobility of the minority carriers (in the given case, of electrons) depends on direction of the current flow. The change in their mobility (if they are considered like majority carriers) occurs in accordance with the expression

$$\mu_n(\hat{u}) = \sum_{m=1}^3 \frac{n^{(m)}(\hat{u})}{n(0)} \mu_n^{(m)}, \quad (6)$$

where $n^{(m)}(\hat{u})$ is population of a pair of the valleys located on the different axes of (100)-type, $\mu_n^{(m)}$ is the electron mobility along the current direction in each of three pairs of these valleys, $n(0)$ is the total electron concentration. Accordingly, at the current in plane of the wafer (along [100] or [010] axis) we obtain

$$\frac{\mu_n^{\text{long}}(\hat{u})}{\mu_n(0)} = \frac{3}{2K+1} \frac{1+K+K \exp(\beta u_{\parallel})}{2+\exp(\beta u_{\parallel})}, \quad (7)$$

and at the current perpendicular to the wafer surface (i.e. along [001] axis) –

$$\frac{\mu_n^{\text{trans}}(\hat{u})}{\mu_n(0)} = \frac{3}{2K+1} \frac{2K+\exp(\beta u_{\parallel})}{2+\exp(\beta u_{\parallel})} \quad (8)$$

where $K = \frac{\mu_l}{\mu_t} \approx \frac{m_l}{m_t}$ is anisotropy parameter for

electron mobility (m_l and m_t are longitudinal and transversal electron effective masses, respectively),

$$\beta = \frac{\Xi_u(S_{11} - S_{12})}{kT(S_{11} + S_{12})}.$$

For numerical calculations, the same values of silicon parameters (as in [4, 5]) are used. In Figs. 1 and 2, two components of the piezojunction effect as functions of the bisotropic strain u_{\parallel} are represented (at room temperature): in Fig. 1 – for squared intrinsic carrier concentration (curve “(001)-wafer”), in Fig. 2 – for electron mobility at two current directions: in the plane of the wafer (longitudinal) and perpendicular to it (transversal).¹ Because the mobility in the transversal direction varies with the strain qualitatively like to the intrinsic concentration then the piezojunction effect at this current orientation is larger than at its longitudinal orientation. This fact is illustrated by Fig. 3 (curves (001)_{trans} and (001)_{long}).

¹ Using by occasion let us notice that in all figures of papers [4, 5] horizontal axes have to be reversed.

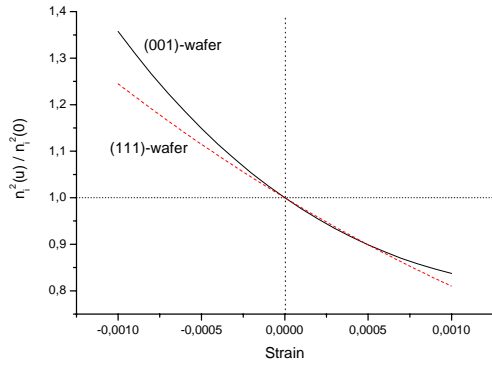


Fig. 1. Relative changes of squared intrinsic carrier concentration with bisotropic strain in silicon wafers of two orientations.

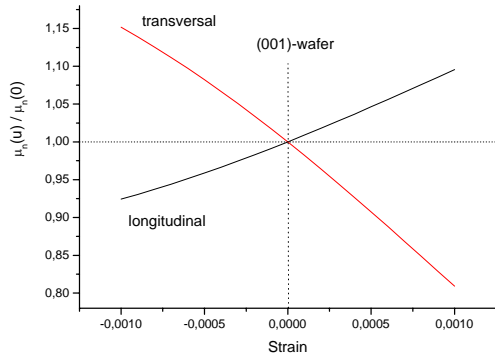


Fig. 2. Relative changes of the electron mobility with bisotropic strain in (001)-Si wafer at the current in plane of the wafer (longitudinal) and perpendicular to it (transversal).

4. The case of (111)-oriented silicon wafer

In this case, it is necessary to transform the initial bisotropic strain into crystallographic coordinate system by means of the transformation matrix [14]

$$\begin{pmatrix} 1/\sqrt{2} & \cos\theta/\sqrt{2} & \sin\theta/\sqrt{2} \\ -1/\sqrt{2} & \cos\theta/\sqrt{2} & \sin\theta/\sqrt{2} \\ 0 & -\sin\theta & \cos\theta \end{pmatrix} \quad (9)$$

at $\sin\theta = \sqrt{2/3}$, $\cos\theta = \sqrt{1/3}$. As a result, for the same in magnitude initial (i.e. in the wafer plane) bisotropic stress (equal to $u_{\parallel}/(S_{11} + S_{12})$)

$$\begin{aligned} u_{xx} = u_{yy} = u_{zz} &= \frac{1}{3} \frac{S_{11} + 2S_{12}}{S_{11} + S_{12}} u_{\parallel}, \\ u_{xy} = u_{yz} = u_{zx} &= \frac{1}{3} \frac{S_{44}}{S_{11} + S_{12}} u_{\parallel}. \end{aligned} \quad (10)$$

Under action of the strain (10) all three pairs of electron valleys take equal shifts

$$\begin{aligned} \delta E_c^{(100)} = \delta E_c^{(010)} = \delta E_c^{(001)} &= \\ = 2(\Xi_d + \frac{1}{3}\Xi_u) \frac{S_{11} + 2S_{12}}{S_{11} + S_{12}} u_{\parallel}, \end{aligned} \quad (11)$$

and shifts in the energy of the valence subbands have the form (4) with different value of the parameter $\varepsilon = \frac{d}{2\sqrt{3}} \frac{S_{44}}{S_{11} + S_{12}} u_{\parallel}$ where d is the 3rd deformation

potential for the valence band. Variation in the electron mobility does not occur at this wafer orientation:

$$\frac{\mu_n^{\text{trans}}(\hat{u})}{\mu_n(0)} = \frac{\mu_n^{\text{long}}(\hat{u})}{\mu_n(0)} = 1;$$

therefore the whole piezjunction effect is determined by variation of the squared intrinsic carrier concentration. The latter is represented by the curve “(111)-wafer” in Fig. 1.

As is followed from comparison of two curves in Fig. 1 (as well as from Fig. 3) p - n junction on (111)-oriented silicon wafer is less subjected to the influence of bisotropic compressions in comparison with (001)-oriented one. As for bisotropic tensions, then (as followed from Fig. 3) their influence in the case of (001)-wafer can be both less and more than in the case of (111)-wafer depending on the current direction.

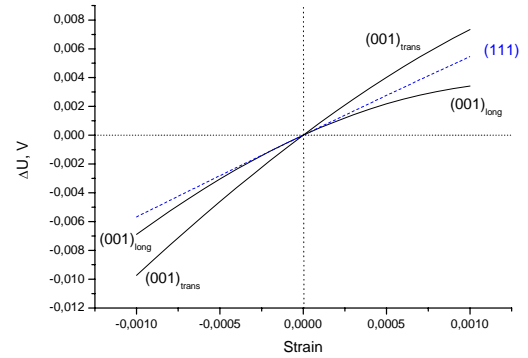


Fig. 3. Changes in the voltage drop across the diode versus bisotropic strain in (001)-Si wafer at the current in plane of the wafer $(001)_{\text{long}}$ and perpendicular to it $(001)_{\text{trans}}$ as well as in (111)-Si wafer (dashed line).

5. The case of strong deformation of (001)-oriented silicon wafer

In recent years, the great attention is attracted to use (for enhancement of electron and hole mobility) of biaxial strains induced in Si layer deposited onto SiGe buffer layer. The strains can achieve very large values. In particular, biaxial tensile strain of 0.8% has been measured in [15]. In the paper [16], the strain amounted to 1%, in [17] – 1.2%. Magnitude of the biaxial strain as large as 1.7% has been quoted in the review [18]. The statement about a method “that allows strain levels of up to and over 2% to be achieved either uniaxially or biaxially” was made in paper [19].

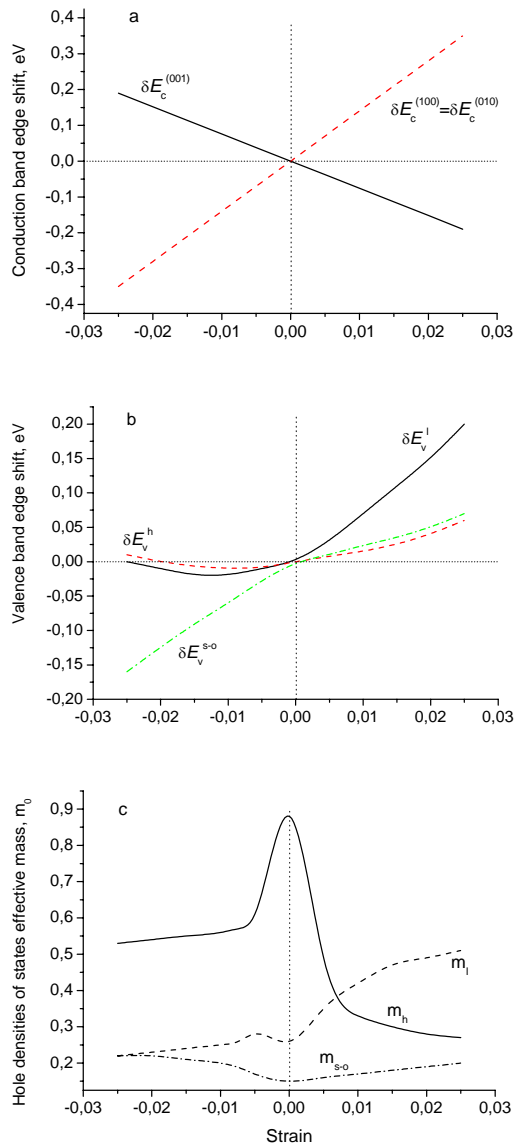


Fig. 4. Dependences of the conduction band edge shifts (a), the valence band edge shifts (b), and the hole densities-of-states effective masses (c) on large bisotropic strains in (001)-Si (from Ref. [20]).

Energy band structure and effective masses for strongly strained (001)-Si have been calculated in the paper [20]. In Fig. 4, these results have been reproduced, for convenience, in our designations. And shown in Fig. 5 is variation of the squared intrinsic carrier concentration calculated in accordance with these data for strain values achieving $\pm 2.5\%$. As is seen from Fig. 5 intrinsic carrier concentration in strained silicon can increase under these conditions by several orders of magnitude (irrespective of deformation sign). So, lightly doped material can be transformed into material with intrinsic conductivity. As for $p-n$ junction diode based on this material, the injection level of the emitter (at the given doping level) can increase, under these conditions, by five orders of magnitude.

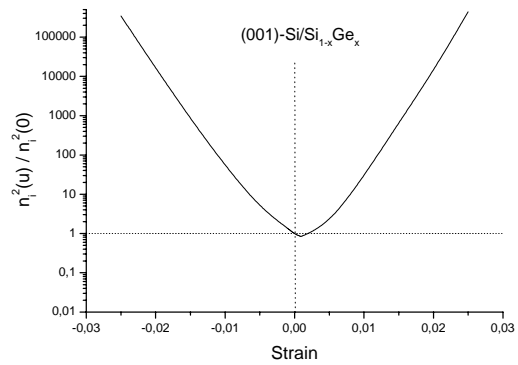


Fig. 5. Relative change of squared intrinsic carrier concentration with bisotropic strain in (001)-Si at large bisotropic strains (as based on the results from Fig. 4).

6. Conclusion

So, as for effect of the bisotropic strains on indications of diode temperature sensors, the diodes on the surface of (111)-Si are less subjected to their influence (in the case of bisotropic compression) in comparison with (001)-oriented Si. Furthermore, though at small strains, the band gap of silicon that determines the intrinsic carrier concentration narrows at bisotropic compressions and broadens at bisotropic tensions, at large bisotropic strains of any sign, narrowing the band gap and significant growth of the intrinsic carrier concentration take place.

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