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Hybrid-integrated version of solid-state components for terahertz frequency region

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Abstract. We present the results of development of key components for THz devices obtained using mathematical modeling of detector sections, radiation source and subharmonic mixers. Schottky barrier diodes with different parameters served as nonlinear elements of the above facilities.

Keywords: THz region, SBD, detector, frequency multiplier, subharmonic mixer, waveguide-to-microstrip line transition, rejection filter, SPICE model.

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1. Introduction

In recent years, much consideration has been given to systems operating at THz frequencies. The interest to THz radiation was dictated by the following facts:

- this radiation is not ionizing (quanta of 0.001–0.01 eV);
- this radiation passes well through turbid media and fine-dispersed materials because of strong suppression of Rayleigh scattering ($1/\lambda^4$);
- the corresponding frequency region is that of molecular rotational spectrum, vibrations of biologically important DNA and proteins, and solid-state plasma vibrations;
- the photon thermal noise is almost absent in that radiation region, and sensor characteristics of detectors become better as frequency grows;
- at THz frequencies, the data receive and transfer rates as well as number of channels are much over those at radio communication.

The functional modules for THz region are key elements when developing novel small-sized THz wireless systems and technologies for radars and radio navigation, radio-wave imaging, medicine, biology, customs and anti-terrorist inspection [1–3].

In our developments, we chose microstrip lines as signal matching and isolation elements, according to the modern technologies and requirements for reduction in price of products. A Schottky barrier diode (SBD) was chosen as nonlinear element of detectors, frequency multipliers and mixers, because SBD has quadratic $I-V$ curve and the lowest loss resistance among the diodes that are applied when making microwave components [3–5].

2. Broadband detector sections with SBD for 220-325 GHz and 325-400 GHz frequency ranges

To exert control over the finite device of radio receiver when receiving modulated signals, the received signal should be transformed in such a way as to make the law of variation of voltage coming to input of the finite device to replicate the signal modulation law. Such a transformation of signal in radio receiver is made by a detector. Amplitude detectors are used to detect unmodulated and amplitude-modulated signals; each of them may be continuous or pulse.

The detector sections are mostly used in microwave measuring devices (in particular, in panoramic meters of standing wave ratio load), indicating devices etc. The main type of most widely used amplitude detectors is that involving a nonlinear

element and a load to which rectified voltage is applied. Semiconductor diodes are widely used in amplitude detectors as nonlinear elements [4, 5].

A block diagram of detector section is presented in Fig. 1.1. A microwave signal (1) comes to an input waveguide flange (2) of detector; input signal power is supplied to a diode (4) using a waveguide-to-microstrip line tapered transition; a modulated signal envelope is removed with a rejection filter (5) to a video amplifier (6).

To develop broadband detector sections, we performed mathematical modeling for impedance characteristics of active and passive elements, microwave integrated circuit (IC) and detector sections with SBD. The models for waveguide-to-microstrip line tapered transitions of detector sections were developed using the node potential method. 3D models of waveguide-to-microstrip line transition are presented in Figs 1.2a and 1.2b.

A waveguide-to microstrip line tapered transition is the input part of detector section. The role of tapered transition is ensuring transformation of transition wave impedance over a broad frequency band and, correspondingly, bringing of microwave signal power to detector diode with minimal losses and correct configuration of electromagnetic field. The tapered transition losses are no more than -0.5 dB for 220–325 GHz and 325–400 GHz frequency ranges, so practically the total microwave signal power will pass the transition and come to the detector diode.

We also performed modeling and development of a low-and-high-pass filter that separates at output the frequency components lying in the given frequency band from the composite signal coming to the input. The rest of frequency components are suppressed (reflected, absorbed or branched) by the filter.

The following functions are performed by the filter in the detector section:

- it ensures virtual short circuiting at the site of detector diode connection to the filter in the operating frequency band;
- it serves as rejection filter RF for microwave signal;
- it serves as low-pass filter for a video signal;
- it ensures diode bias.

Shown in Figs 1.3a and 1.3b are filter topologies made on the basis of half-wave resonators with open ends. The sizes of filter elements for the 220–325 GHz and 325–400 GHz frequency ranges were determined using the node potential method. We also calculated frequency dependences of the transmission coefficient S_{21} for the topologies developed. Such filters introduce attenuation over 40 dB at the detector input frequency.

Based on detector diode modeling, we made detector sections for the required ranges. Shown in Figs 1.4 and 1.5 are conversion factors as function of frequency. The detector sections for the 220–325 GHz (325–400 GHz) frequency range ensure conversion factor within 1000–115 V/W (120–102 V/W).

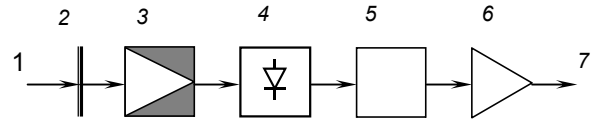


Fig. 1.1. Block diagram of detector section: 1 – microwave signal, 2 – input waveguide flange, 3 – waveguide-to-microstrip line tapered transition, 4 – SBD, 5 – rejection filter, 6 – video amplifier, 7 – output voltage.

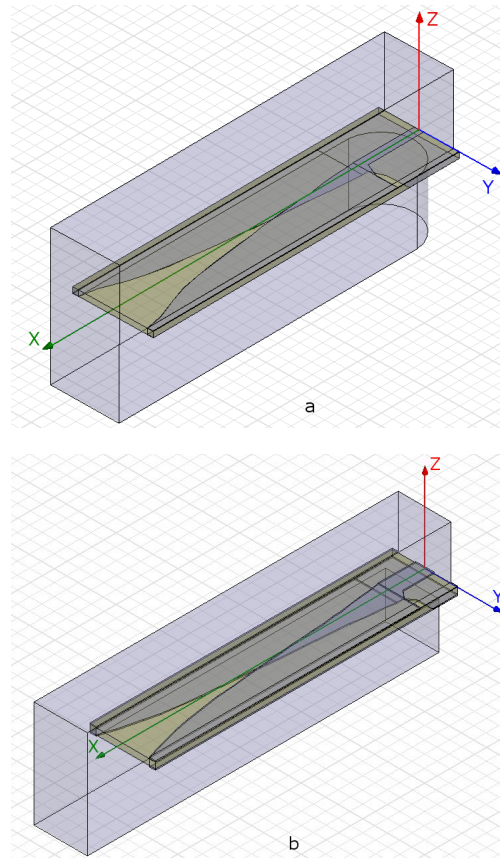


Fig. 1.2. 3D model of waveguide-to-microstrip line tapered transition: a) for a 220–325 GHz detector section [6]; b) for a 325–400 GHz detector section.

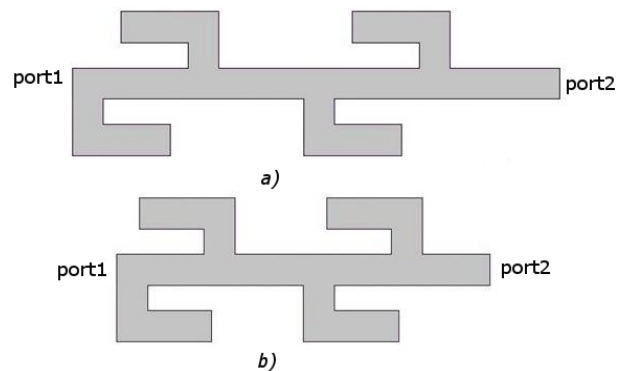


Fig. 1.3. Topology of a rejection filter: a) for 220–325 GHz [6]; b) for 325–400 GHz.

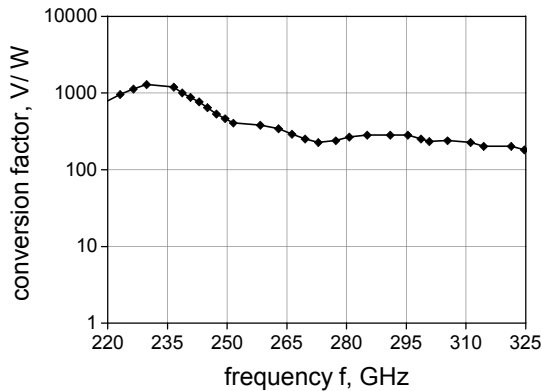


Fig. 1.4. Conversion factor of detector section as a function of frequency in the 220–325 GHz range.

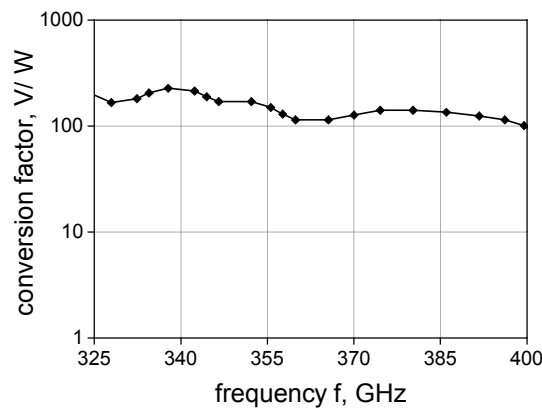


Fig. 1.5. Conversion factor of detector section as a function of frequency in the 325–400 GHz range.

3. Radiation sources using a frequency multiplier with SBD for 280 GHz operating region

To transmit a stable wanted signal at a frequency over that given by an oscillator with the required stabilization level, a frequency multiplier is used. Synthesizing of frequency spectrum also requires frequency multipliers. Their application ensures the following advantages:

- a single-frequency master-oscillator can operate at lower (by several times), thus having better stability;
- it is possible to broaden the tuning range of transmission facility, at narrower range of the single-frequency master-oscillator;
- it is possible to improve stability of device operation by decreasing feedback because input and output circuits of the frequency multiplier are tuned to different frequencies;
- a possibility to increase absolute deviation of frequency (phase) at frequency (phase) modulation appears [7].

Operation of frequency multiplier may be expressed by Fourier transform, with signal being of the form [7]:

$$x(t) = \sum_{k=-\infty}^{\infty} c_k e^{i2\pi kft}, \quad (1)$$

where the k -th harmonic is expressed with period T :

$$c_k = \frac{1}{2\pi} \int_0^T x(t) e^{i2\pi kft} dt. \quad (2)$$

The required harmonic is chosen using a broadband filter.

To develop and fabricate radiation sources based on frequency multipliers with SBD, we performed mathematical modeling of impedance and energy characteristics of active and passive elements of radiation sources. The basis for modeling is the energy diagram of metal–semiconductor contact that describes energy barrier in semiconductor near-contact layer that is responsible for potential difference in the semiconductor near-surface layer.

An active element (SBD) may be described by the Shockley equation supplemented with dependences that describe various physical effects. Modeling of diode operation with the corresponding equations was performed using the SPICE model [8, 9]. Modeling of impedance and energy characteristics of passive elements involves determination of parameters of transmission line followed by its calculation with the node potential method. In that way we estimated, in particular, losses in the transmission line as well as developed topology of IC elements.

The substrate material was duroid with the following parameters: permittivity $\epsilon_r = 2.2$ and $\tan(\delta) = 0.0009$. Using this material, we made a microstrip transmission line. To realize frequency multiplier, we developed the following elements: (i) filters for bringing diode bias voltage, (ii) a rejection filter for 280 GHz and (iii) a microstrip line-to-rectangular waveguide transition for 280 GHz. The requirements to the filter for bringing diode bias voltage are such: (i) bringing voltage supply (U_{bias}) to the active element and (ii) microwave signal transmission from the input (RF In) to the output (RF Out) with minimal losses.

Fig. 2.1 shows topology of such filter. It is perceived that this filter will be set after the active element (IMPATT diode). The filter adds minimal losses at the frequency 280 GHz or 360 GHz. The sizes of such filter elements for the frequencies chosen were obtained with the node potential method.

Since the metallization layer in such topology is continuous, bringing of a direct voltage or low-frequency modulation signal from the input U_{bias} occurs automatically. Another requirement is also fulfilled. An analysis shows that the losses in such filters are no more than 0.42 dB (0.32 dB) at a frequency of 280 GHz (360 GHz).

The rejection filter topology is presented in Fig. 1.3a. This filter is intended for active element isolation from IC input at a high output frequency.

Besides, it lets the master frequency through with minimal losses. The sizes of such filter elements for 280 GHz were obtained with the node potential method. The losses in such filter are over 50 dB at the multiplier output frequency. At the same time, in both cases the added losses are no more than 0.5 dB at the reference oscillator frequency (140 GHz).

Shown in Fig. 2.2 is a construction of a microstrip line-to-rectangular waveguide transition. Such transition ensures microwave signal transmission from the input (3) to the waveguide output (1) (cross-section of 0.8×1.6 mm) with minimal losses at the output frequency. The sizes of transition elements for 280 GHz frequency were obtained with the node potential method [8]. Such transition adds losses no more than 0.5 dB; this value is tolerable.

Fig. 2.3 shows topology of a frequency multiplier with SBD and all its elements. The purposes and parameters of separate parts were described above. This topology is used when designing photomasks intended for fabrication of IC test specimens. Shown in Fig. 2.4 is appearance of radiation source with frequency multiplier. Fig. 2.5 presents a block diagram of radiation source components with frequency of 280 GHz at the multiplier. The technical characteristics of the radiation source with a frequency multiplier are given in Table.

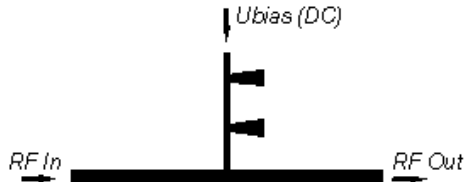


Fig. 2.1. Topology of a filter for supply of diode bias voltage [6].

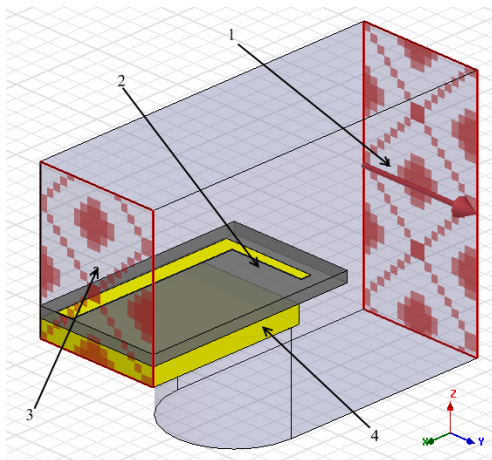


Fig. 2.2. Construction of a microstrip line-to-rectangular waveguide transition: 1 – rectangular waveguide section; 2 – microstrip line-to-waveguide transition (excitation loop); 3 – microstrip line intersection; 4 – microstrip line [6].

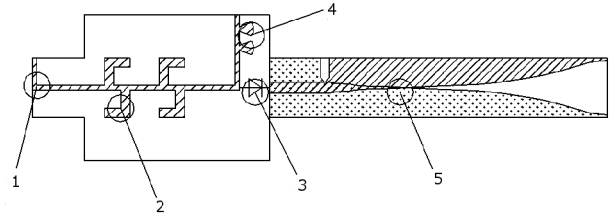


Fig. 2.3. Topology of a frequency multiplier: 1 – waveguide excitation loop at frequency multiplier output (280 GHz); 2 – rejection filter; 3 – SBD; 4 – supply filter; 5 – waveguide-to-microstrip line transition for input signal (140 GHz) [6].



Fig. 2.4. Radiation source with frequency of 280 GHz at the multiplier.

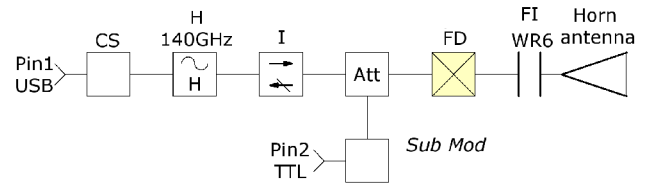


Fig. 2.5. Block diagram of radiation source components with multiplier frequency of 280 GHz: CS – current source; H – heterodyne with IMPATT diode, $P_{out} = 30$ mW; I – isolator; Att – modulator with *p-i-n* diodes; FD – frequency doubler; FI – outlet flange WR6, UG387; Sub Mod – submodulator.

Table. Source technical characteristics.

#	Parameters	Values
1	Operating frequency, GHz	280
2	Output power, mW	0.3...0.5
3	Amplitude modulation frequency, kHz	1
4	Relative instability	$10^{-4} \dots 10^{-5}$
5	Supply voltage, V	220 (50 GHz)
6	Operating temperature range, °C	+5...+40

4. Subharmonic mixers with SBD for 220–325 GHz and 325–400 GHz operating frequency ranges

The problem of development of receivers for submillimeter and THz regions is urgent. Such devices are per se heterodyne detectors whose most important element is frequency converter. The latter is a facility for transfer of signal spectrum to another frequency region,

with retention of the modulation law. To this end, a nonlinear element and local heterodyne are used to ensure operation mode of the conversion device. It is reasonable to use semiconductor SBDs as nonlinear elements in frequency converters. The slope of diode $I-V$ curves varies with a rather high signal U_H from heterodyne and remains practically the same under action of a low signal U_s ($U_s \ll U_H$). Frequency multiplication is realized owing to the signal effect of on the slope of $I-V$ curve of diode that is a part of mixer.

Stability of both frequency and power level of heterodyne is a significant requirement. The problem of development of highly stable heterodyne for frequencies over 200 GHz and required power level is rather complicated. But it is possible to use the mixer property to operate at a heterodyne frequency half as high as useful signal. A mixer with subharmonic pumping that operates at the second harmonic of heterodyne signal makes it possible to halve the signal operating frequency and thus to essentially simplify realization of the required power level at a lower frequency.

By applying the four-terminal network theory and analyzing operation of transceiver radio channel, the following conclusions have been drawn: (i) to minimize receiver noise factor, one should ensure maximization of transmission coefficient and minimization of mixer noise factor; (ii) to increase power level of transmitter signal, one should maximize transmission coefficient. Thus, taking into account that conversion losses of a passive mixer are approximately equal to the noise factor, the main criterion for design of mixer is minimization of conversion losses.

We used the hybrid mixer topology and chose for nonlinear element an SBD with beam leads obtained by metal layer deposition onto semiconductor surface [10, 11]. This diode has quadratic $I-V$ curve and the lowest loss resistance among the diodes used when making microwave mixers [12, 13]. For the chosen diodes of different capacitances we measured $C-V$ curves (Fig. 3.1a) and $I-V$ curves at the forward branch (Fig. 3.1b). Fig. 3.1a shows that in SBD for mixers (contrary to the frequency multiplier diodes) $C-V$ curves are practically straight line. The loss currents did not exceed 1 nA.

Mathematical modeling of SBD was performed using the SPICE model. The latter attributes diode $I-V$ curve to its physical parameters. A diode SPICE model involves a number of mathematical functions that describe diode operation at different initial parameters. SBD (at which a wanted signal and that from the heterodyne come) is approximated by an equivalent circuit, with account made for the initial parameters. An analysis is made by constructing equation for circuit using the modified node potential method [9]. The initial diode parameters were chosen from the experimental $C-V$ curves and $I-V$ curves.

We performed modeling of board topology for microwave mixers with a rejection filter and waveguide-

to-microstrip line tapered transition using the node potential method. The general appearance of board for microwave mixers is presented in Fig. 3.2.

Based on mathematical modeling of diode operation and 3D numerical modeling of electromagnetic field in the mixer made with the node potential method, we developed mixer test specimens for the 220–325 GHz and 325–400 GHz frequency ranges. Shown in Figs 3.3a and 3.3b are appearances of mixer test specimens.

The mixers involve a case, board and microwave mixer SBD with beam leads. A board and microwave SBD are mounted in the mixer waveguide channel. The board material is quartz 25 μm thick, metallized on both sides. The metallization thickness was 3 μm (1 μm) for the 220–325 GHz (325–400 GHz) frequency range. For the 220–325 GHz frequency range, the cross-section of waveguide channel of heterodyne input (signal input) is 1.2×2.4 mm (0.4×0.8 mm), while for the 325–400 GHz frequency range, the corresponding cross-section is 0.8×1.6 mm (0.36×0.71 mm).

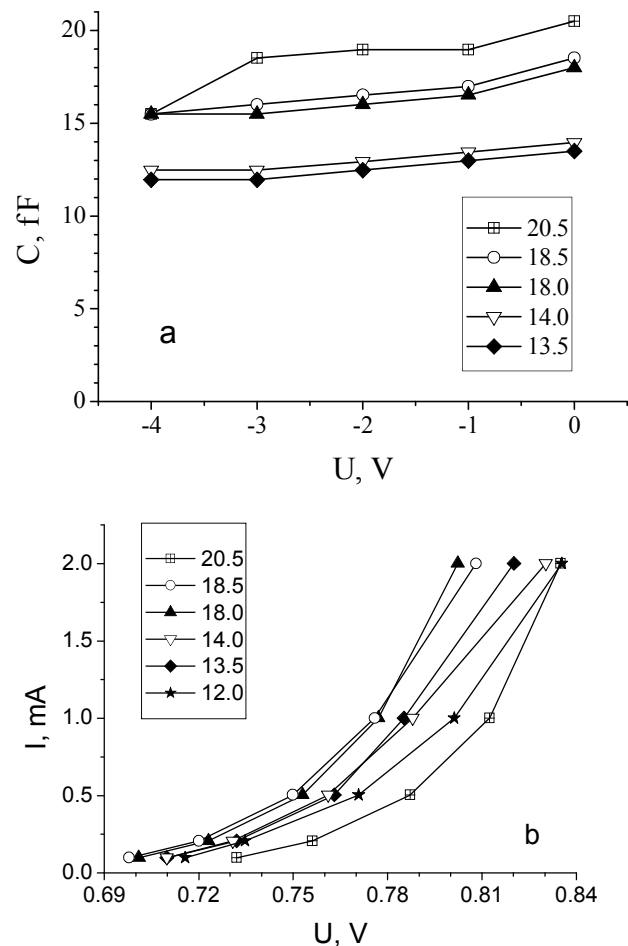


Fig. 3.1. a) $C-V$ curves of SBD with different values of metal-semiconductor contact capacitance C_{jo} (in femtofarads – see inset); b) $I-V$ curves of SBD with different values of metal-semiconductor contact capacitance C_{jo} (in femtofarads – see inset).

We studied the electric parameters of the developed mixers. The results of measurement of conversion losses as a function of heterodyne power are presented in Figs 3.4a and 3.4b. One can see that conversion losses K firstly decrease as heterodyne power (P_H) grows and then this trend becomes slower. For subharmonic mixers for the 220–325 GHz frequency range the conversion losses are below 30 dB at the maximal heterodyne power of 25 mW, while for subharmonic mixers for the 325–400 GHz frequency range the conversion losses begin to flatten out at heterodyne power over 15 mW and are below 30 dB at the maximal heterodyne power of 18 mW. Further grows of heterodyne power leads to burning out of SBD with beam leads [9].



Fig. 3.2. Microwave mixer board.

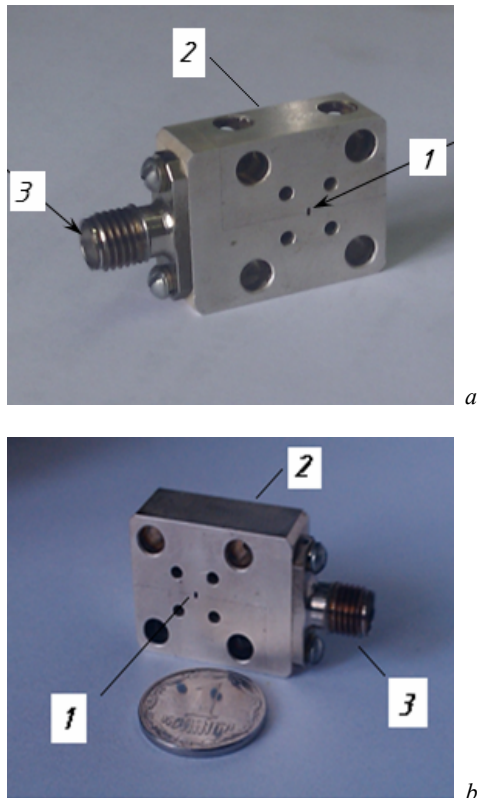


Fig. 3.3. Mixer test specimens for 220–325 GHz frequency range [14]. (a): 1 – waveguide input of signal (0.4×0.8 mm); 2 – waveguide input of heterodyne (1.2×2.4 mm); 3 – intermediate frequency (IF) and 325–400 GHz frequency range. (b): 1 – waveguide input of signal (0.36×0.71 mm); 2 – waveguide input of heterodyne (0.8×1.6 mm²); 3 – IF output.

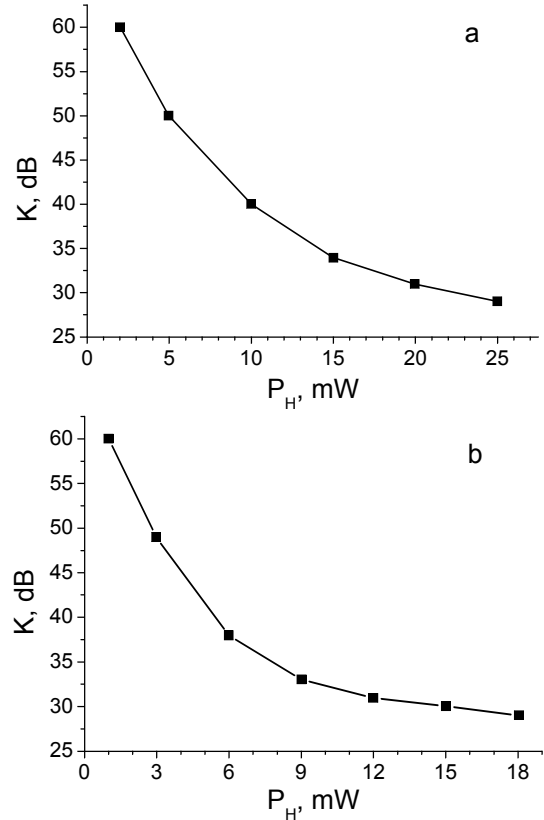


Fig. 3.4. Subharmonic mixer conversion losses K as a function of heterodyne power P_H : a) for 220–325 GHz range at a frequency of 280 GHz [14] and b) for 325–400 GHz range at a frequency of 360 GHz.

5. Conclusions

The following equipment has been made in the course of execution of the research project according to the Government Special-Purpose Scientific and Technical Program “Nanotechnologies and Nanomaterials” in 2010–2013:

- detector sections for the 220–325 GHz and 325–400 GHz frequency ranges that ensure conversion factor within 1000–115 V/W and 120–102 V/W, respectively;
- radiation source using master-oscillator with a silicon IMPATT diode (operating frequency of 140 GHz) and frequency doubler with SBD (operating frequency of 280 GHz and output power of 0.3–0.5 mW);
- subharmonic mixers for the 220–325 GHz and 325–400 GHz frequency ranges, with conversion losses no more than 30 dB.

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