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Trap-assisted conductivity in anodic oxide on InSb

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Abstract. The direct current conductivity of anodic oxide of InSb has been investigated as a function of applied bias and temperature. Proposed in this work is a model of conductivity that includes ohmic, trap-assisted tunneling and Poole–Frenkel conduction processes. Two defect states were found in the energy gap of the anodic oxide, which can be attributed to bulk traps. The asymmetry in the current-voltage characteristics is analyzed in terms of comparative distribution of the applied bias voltage between the anodic oxide and the depletion region in InSb.

Keywords: InSb, anodic oxide, traps, conductivity.

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1. Introduction

Narrow-gap InSb is one of the key semiconductor materials that is widely used to manufacture modern infrared detectors [1]. Since InSb is characterized by relatively low melting point, deviation from stoichiometry at the surface as a result of evaporation or segregation of volatile component (Sb) can occur at rather low temperatures. Therefore the surface passivation and protection is recognized to be an important step in technology of InSb infrared detectors. The passivation layer deposited on the surface of infrared photodiodes should provide high resistance, low interface state density and low fixed charge density as well as satisfy requirements for both *n*-type and *p*-type conductivity surface simultaneously. As was first shown by Dewald [2], anodic oxides, grown by the electrolyte technique, can be used as effective passivation layers for InSb photodiodes. Since that, numerous studies of InSb anodic oxides have been published in the literature (see, e.g., [2-10] and citations therein). It was shown that the most important feature of InSb anodization is the composition inhomogeneity of anodic oxides, which strongly depends on the growth conditions. Analysis of published data indicates that physical properties of

anodic oxides on InSb are dependent on a number of factors such as quality and crystallographic orientation of initial wafers, surface treatment before anodization, parameters of anodization process, thermal treatment of oxides after anodization, *etc.* Because of anodic oxides on InSb have poor chemical and mechanical stability, the passivation stacks comprised the anodic oxide and extrinsic dielectric are usually used. For this purpose, thin layers of SiO_x and Si₃N₄ were deposited on the anodic oxide by different low-temperature physical and chemical vapor deposition techniques [6-8]. The density of interface states in the range 10¹¹...10¹² cm⁻²·eV⁻¹ was shown to be typical for the anodic oxide on InSb. Lower densities were obtained when the thermally grown native oxide on the substrate surface was preserved before anodization [6, 9]. The anodic sulfidization process also provides rather low density interface states of the order of 3·10¹⁰ cm⁻²·eV⁻¹ [10].

The commonly used approach for investigation of the anodic oxide on InSb is based on preparation of MIS-type capacitors. This allows one to use well developed experimental techniques for characterization of anodic oxides themselves as well as their interface with a semiconductor [11]. The direct current conductivity in MIS capacitors of InSb was studied in a number

of works [12-16]. Because of contradictory data were obtained, studies of electrical conductivity remain important in order to clarify the role of native defects in the anodic oxide. The aim of this work was to study conductivity mechanisms and aging effects in MIS capacitors of InSb.

2. Samples and experimental methods

The MIS capacitors were prepared on *n*-InSb(100) substrates with electron concentration in the range $(1...2) \cdot 10^{14} \text{ cm}^{-3}$ at the temperature 77 K. Before anodization, all substrates were mechanically and chemo-mechanically polished on a soft pad wetted with 0.5% Br₂ in methanol. Finally, they were immersed into Br₂ in methanol solution for several minutes. In this way, a shiny mirror-like surface was obtained. The anodic oxide was grown using an electrolyte of 0.1N·KOH in a mixture of 90% ethylene glycol and 10% H₂O. The anodization process was carried out in two steps. At the first step, the oxide was grown in a galvanostatic mode at a current density 0.1 mA/cm² until the voltage of 28 V on the electrochemical cell was reached. At the second stage, potentiostatic anodization was continued at 28 V until anodic current drops down to $\sim 10 \mu\text{A}/\text{cm}^2$. As measured by ellipsometry, the thickness of the grown oxide was about 450 nm. Indium electrodes with a diameter of 0.75 mm were deposited on the oxide surface by vacuum evaporation.

In order to investigate aging effects, the capacitors were thermocycled between 77 K and room temperature for several (typically 5-7) complete cycles. Each thermocycle included fast cooling down to 77 K and slow warming to room temperature. The conductivity was investigated by measuring the direct current flowing through the anodic oxide as a function of the applied bias and temperature within the range 77...300 K.

3. Results and discussion

Three groups of MIS capacitors were investigated: initial, subjected to thermal cycling and stored after cycling for one-month period. The current-voltage characteristics of representative capacitors from each group are shown in Fig. 1. The voltage is assumed to be positive, when the indium electrode on the anodic oxide is positively biased with respect to InSb substrate. The following features of the measured characteristics should be pointed out. The thermal cycling results in increase of the anodic oxide conductivity, that is decreased to the initial state after one-month storage in the laboratory conditions. The measured current is polarity dependent (Fig. 1). At low biases, the current-voltage characteristics exhibit ohmic behavior, whereas at higher biases the nonlinear behavior is observed. Note that the maximum values of the forward current in Fig. 1 correspond to the breakdown voltages 7, 8 and 3 V for the initial capacitor, after storage and thermal cycling, respectively.

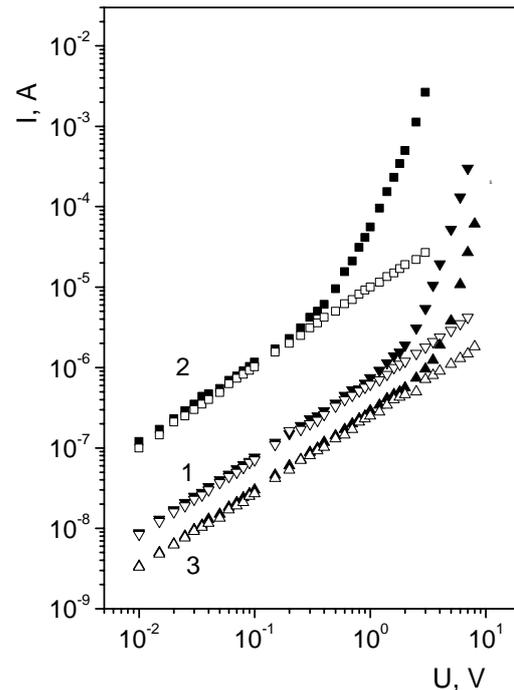


Fig. 1. Reverse (open dots) and forward (close dots) current-voltage characteristics in initial (1), subjected to thermal cycling (2) and stored for one month (3) InSb MIS capacitors.

The temperature dependences of the leakage current, measured at fixed values of forward and reverse biases, are shown in Fig. 2. As can be seen, there are two temperature intervals where activation dependences of the current on temperature are observed. The activation energies determined from their slope were $E_1 = 0.08 \text{ eV}$ and $E_2 = 0.21 \text{ eV}$ at low forward and reverse bias voltages, which correspond to the linear I vs U dependences in Fig. 1. These energies decrease to 0.06 and 0.16 eV at the bias voltage 3.0 V, which corresponds to the nonlinear I vs U dependences. After thermal cycling, the corresponding energies were 0.05 and 0.17 eV as well as 0.04 and 0.14 eV at low and high biases, accordingly. The observed activation conductivity can be attributed to thermal ionization of traps in the anodic oxide (hereafter shallow and deep traps). Note that presence of two sets of traps in the anodic oxide of InSb was previously reported in the literature [5].

As a rule, the polarity dependent current-voltage characteristics of MIS capacitors is interpreted by Schottky emission [11]. This conductivity mechanism was analyzed in details in InSb MIS capacitors [13, 14]. To fit the experimental current-voltage characteristics at room temperature the Schottky current density was calculated using the formula

$$J_S = A^* T^2 \left(m^* / m_0 \right) \exp \left[- \frac{e}{kT} \left(\Phi_B - \sqrt{\frac{eF}{4\pi\epsilon_\infty\epsilon_0}} \right) \right] \quad (1)$$

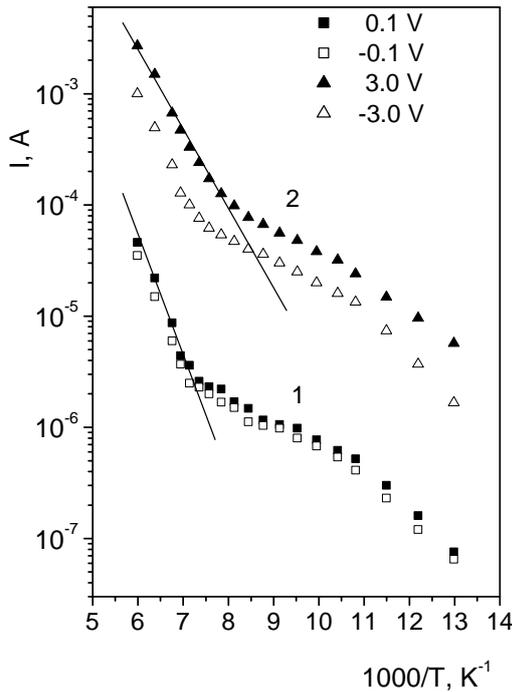


Fig. 2. Temperature dependences of dark current measured before (1) and after (2) thermal cycling. The legend shows the bias voltages in volts.

for the effective mass of electrons in the anodic oxide $m^* = 0.01m_0$ and the potential barrier height at the interface Φ_B close to 1.0 eV [13]. It should be pointed out that the fitting of calculated data was obtained only at room temperature by using the rather low value of the electron effective mass. In this study, the electron effective mass was estimated in the following way. Because of the anodic oxide is composed of In_2O_3 and Sb_2O_3 , both constituents can contribute to the effective mass. Due to In_2O_3 to Sb_2O_3 ratio depends on the preparation conditions and remains unknown in the particular sample, the following simplification was made. It was taken into account that In_2O_3 constituent prevails in the anodic oxide [6]. Further, it is generally believed that the oxide is amorphous in its nature. However, presence of nanocrystallites in the amorphous phase and formation of microcrystalline structure were observed as well [5, 16]. So, the values of electron effective mass $m^* = 0.2 \dots 0.3m_0$, which was reported in the literature for polycrystalline films and single crystals [18-20], were assumed to be suitable for amorphous In_2O_3 . These values are typical for electrons in absence of degeneracy. At room temperature, the critical carrier concentration for the onset of degeneracy in In_2O_3 is approximately equal to $2 \cdot 10^{18} \text{ cm}^{-3}$ [21]. A similar value of $5.5 \cdot 10^{18} \text{ cm}^{-3}$ was also obtained in [20]. In the presence of degeneracy, the electron effective mass in In_2O_3 strongly depends on the electron concentration due to conduction band nonparabolicity.

The results of Schottky emission current calculation (not shown in Fig.1) are restricted to the following reasons. At the temperature 77 K, the

calculated values are much smaller as compared to those in Fig. 1, when the barrier height of the order of 1 eV was used. The huge difference between the experimental and calculated values indicates that the asymmetry of $I-U$ characteristics has another nature. In addition, it must be stressed that the Schottky emission may be ineffective in these capacitors with the large thickness of the anodic oxide, because there is the possibility for the injected electrons to be captured to the bulk traps. It is natural to assume that in this case the leakage current is controlled by the Poole–Frenkel emission. In this conductivity mechanism, the charge transfer between localized traps is triggered by field-induced lowering the potential barrier height for the carriers captured in the traps [11]. Obviously, the conductivity process is hopping in nature and particularly relevant for MIS capacitors with thick oxide, where the relative importance of Schottky emission decreases. The Poole–Frenkel effect is assumed to be applicable for the neutral traps only [22]. Potential barrier lowering occurs as a result of the Coulomb interaction between the released electron and the arising charged trap. For the singly ionized trap, it is given by the following expression:

$$\Delta\Phi_{\text{PF}} = \beta_{\text{PF}} F^{1/2} = \left(\frac{e^3}{\pi\epsilon_{\infty}\epsilon_0} \right)^{1/2} F^{1/2}, \quad (2)$$

where F is the electric field strength, ϵ_{∞} – optical dielectric constant. Other symbols have their usual meanings. With the lowered barrier, the concentration of thermally activated carriers n_0 increases yielding the Poole–Frenkel current density [22]

$$J_{\text{PF}} = en_0\mu F \exp\left(\frac{\Delta\Phi_{\text{PF}}}{kT}\right), \quad (3)$$

where n_0 is the thermally equilibrium concentration of electrons, μ – electron mobility. The often cited current density is represented as [11]

$$J_{\text{PF}} \propto F \exp\left(-\frac{\Phi_0 - \beta_{\text{PF}} F^{1/2}}{kT}\right), \quad (4)$$

where Φ_0 is the energy for the electron transition from the neutral trap to the conduction band. Obviously, experimental $I-U$ curves should be represented by the straight line in the coordinates $\ln(J_{\text{PF}}/F) - F^{1/2}$, if this mechanism is dominant. The slope of the straight line β_{PF} is usually used for estimation of the optical dielectric constant ϵ_{∞} . In the case of weak compensation of the donor-like traps the current density is given by

$$J_{\text{PF}} = e(N_c N_t)^{1/2} \mu F \exp\left(-\frac{\Phi_0 - \beta_{\text{PF}} F^{1/2}}{2kT}\right), \quad (5)$$

where N_t is the density of traps, N_c – effective density of states in the conduction band [23]. The current-voltage characteristics calculated using the formula (3) are shown in Fig. 3. The calculated curves were obtained for the mean value of the electron mobility $\mu = 7 \text{ cm}^2/\text{V}\cdot\text{s}$ reported for amorphous In_2O_3 films [18-20] and the optical dielectric constant $\epsilon_\infty = 14$. The electron concentration n_0 served as an adjusting parameter.

The Poole–Frenkel conductivity in the investigated capacitors can be explained as follows. At low electric field, ohmic conductivity occurs due to hopping of thermally excited electrons between the traps. With the bias increase, electrons are injected from the InSb substrate to the conduction band of the oxide through the traps located near the interface. The drift of injected electrons in the conduction band, their capture to the bulk traps and subsequent release from the traps result in the Poole–Frenkel conductivity. The injected electrons can be captured by shallow and deep traps, but shallow traps seem to be more effective in the charge transfer at low temperature. However, when the capacitor is heated from 77 K to room temperature emptying of deep traps occurs. Being excited to the conduction band, electrons have large probability to be captured by shallow traps and surface defects. This results in the conductivity increase as well as the shift of $C-U$ curve in Fig. 4 towards the positive bias voltage. In the process of prolong storage at room temperature, the initial distribution of electrons between the deep and shallow traps is restored. In general terms, the proposed model of conductivity correlates with those previously discussed in the literature [5, 24, 25].

To explain the polarity dependent current-voltage characteristics in Fig. 1, one should point out that the investigated capacitors are characterized by rather low resistance of the anodic oxide. Due to the fact that the negatively charged interface results in formation of the depletion region in InSb, the resistance of the substrate and anodic oxide may be comparable. In the forward biased capacitors, the resistance of the depletion region decreases and the applied bias is mainly dropped on the anodic oxide yielding the nonlinear $I-U$ characteristics. At the reverse bias, smaller portion of the applied bias drops on the oxide, which results in the linear (ohmic) $I-U$ characteristics.

Another reason for the asymmetry of the current-voltage characteristic may be as follows. The composition inhomogeneity in the anodic oxide can cause the electrical inhomogeneity due to formation of graded band gap and non-uniform distribution of charged defects. Since Ga_2O_3 has the larger band gap in comparison with In_2O_3 [26-28], built-in electric field can appear in the anodic oxide. The presence of this built-in electric field can explain the spontaneous motion of carriers injected from the interface into the anodic oxide at a distance comparable to the oxide layer thickness [25]. To clarify the situation, additional investigations are needed.

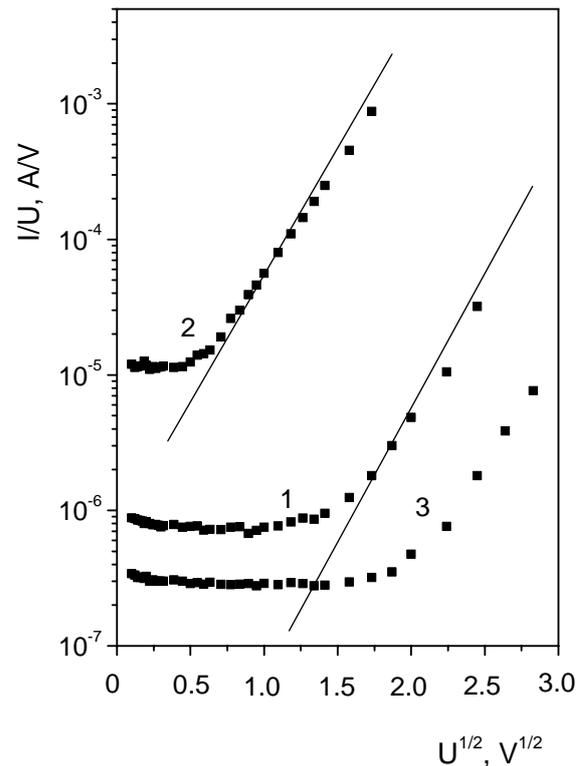


Fig. 3. Measured (dots) and calculated (lines) current-voltage characteristics in initial (1), subjected to thermal cycling (2) and stored for one month (3) InSb MIS capacitors in the Poole–Frenkel coordinates. Parameters for calculation: $\epsilon_\infty = 14$, $\mu = 7 \text{ cm}^2/\text{V}\cdot\text{s}$, $n_0 = 3 \cdot 10^6 \text{ cm}^{-3}$ (1) and $5 \cdot 10^9 \text{ cm}^{-3}$ (2).

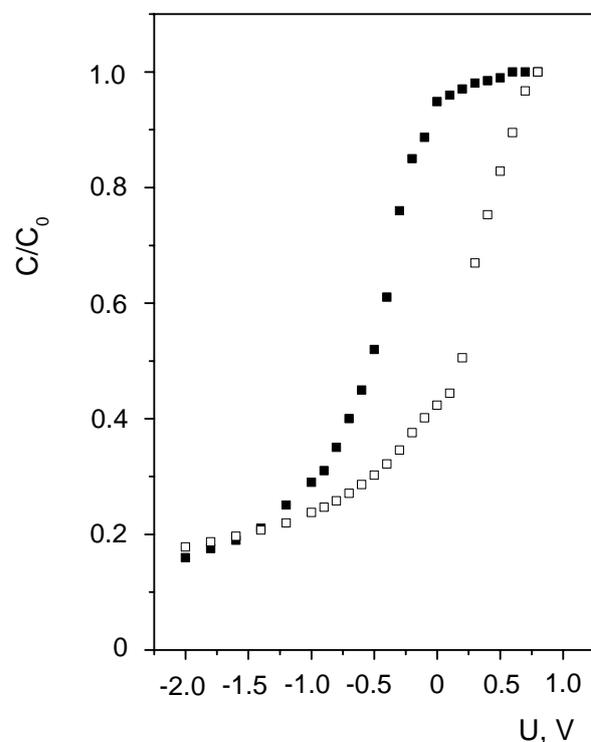


Fig. 4. High-frequency, 1 MHz, capacitance-voltage characteristic in MIS capacitor before (close dots) and after (open dots) multiple thermal cycling.

4. Conclusions

1. The current-voltage characteristics in MIS capacitors of InSb are shown to be polarity dependent. The forward current versus voltage dependences comprise the linear and nonlinear parts, whereas the reverse current linearly depends on the applied bias.

2. Asymmetry and nonlinear behavior of the current-voltage characteristics are explained by comparative contribution of the anodic oxide and the substrate of *n*-InSb to the total resistance of MIS capacitors. The depletion region is caused by the negative fixed charge localized in the anodic oxide and interface.

3. Comparing the measured and calculated current-voltage characteristics, better coincidence was obtained for the Poole-Frenkel emission mechanism of direct current conductivity in the anodic oxide.

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